

FIG. 16 is a block diagram illustrating a system architecture. The system is divided into three main functional areas: an **EXTERNAL MEMORY DEVICE** (10), a **CONNECTION NETWORK** (20), and an **ELECTRONIC COMPUTER** (30).

The **EXTERNAL MEMORY DEVICE** (10) is connected to the **CONNECTION NETWORK** (20). The **CONNECTION NETWORK** (20) is a central hub that facilitates communication between the external memory and the electronic computer.

The **ELECTRONIC COMPUTER** (30) is the central processing unit, which is further divided into several sub-components:

- CONTROL DEVICE** (60): This component is connected to the **CONNECTION NETWORK** (20) and the **PROCESSING DEVICE** (80). It receives **COMMAND SIGNAL** and **INTERRUPT SIGNAL** from the network and the processing device, respectively.
- PROCESSING DEVICE** (80): This component is connected to the **CONTROL DEVICE** (60) and the **PROCESSING DEVICE** (90). It contains a **MEMORY UNIT** and a **PROCESSOR**.
- PROCESSING DEVICE** (90): This component is connected to the **PROCESSING DEVICE** (80) and the **PROCESSING DEVICE** (100). It contains a **PROCESSOR**.
- PROCESSING DEVICE** (100): This component is connected to the **PROCESSING DEVICE** (90) and the **PROCESSING DEVICE** (110). It contains a **PROCESSOR**.
- PROCESSING DEVICE** (110): This component is connected to the **PROCESSING DEVICE** (100) and the **PROCESSING DEVICE** (120). It contains a **PROCESSOR**.

The **PROCESSING DEVICE** (80) is connected to the **CONNECTION NETWORK** (20) via a **COMMAND SIGNAL** and an **INTERRUPT SIGNAL**. The **PROCESSING DEVICE** (90) is connected to the **CONNECTION NETWORK** (20) via a **COMMAND SIGNAL** and an **INTERRUPT SIGNAL**. The **PROCESSING DEVICE** (100) is connected to the **CONNECTION NETWORK** (20) via a **COMMAND SIGNAL** and an **INTERRUPT SIGNAL**. The **PROCESSING DEVICE** (110) is connected to the **CONNECTION NETWORK** (20) via a **COMMAND SIGNAL** and an **INTERRUPT SIGNAL**. The **PROCESSING DEVICE** (120) is connected to the **CONNECTION NETWORK** (20) via a **COMMAND SIGNAL** and an **INTERRUPT SIGNAL**.